



Docket No. BU998050DIV3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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BT  
12-10-03

re patent application of

William P. Moore, et al.

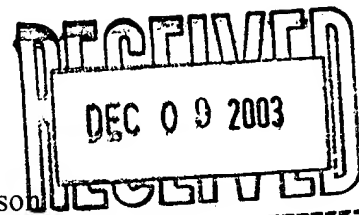
Serial No.: 09/805,138

Group Art Unit: 2184

Filing Date: March 14, 2001

Examiner: Yolanda L. Wilson

For: MICROPROCESSOR INCLUDING CONTROLLER FOR REDUCED POWER  
CONSUMPTION AND METHOD THEREFOR



Honorable Commissioner of Patents  
Alexandria, VA 22313-1450

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DEC 0 8 2003

Technology Center 2100

INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents listed on the attached from PTO-1449, to the attention of the Examiner in charge of the above-identified application. All of these references were cited by the Examiner in co-pending Application No. 09/805,137, in an Office Action dated November 10, 2003.

This citation does not constitute an admission that the references are relevant or material to the claim. They are only cited as constituting related art of which the applicant is aware.

I hereby certify that no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in §1.56(c) more than three months prior to the filing of the information disclosure statement.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Assignee's Deposit Account No. 09-0456.

Respectfully submitted,

*Peter A. Balnave*

Peter A. Balnave  
Registration No. 46,199

Date: 12/5/03  
McGinn & Gibb, PLLC  
Intellectual Property Law  
8321 Old Courthouse Road, Suite 200  
Vienna, VA 22182-3817  
(703) 761-4100  
Customer No. 21254